



PATENT APPLICATION
Do. No. 5038-331

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Douglas D. Boom and Matthew M. Gilbert

Serial No.: 10/698,144

Examiner: Not yet known

Filing Date: October 3, 2000

Group Art Unit: 2188

Title: ALLOCATION OF PROCESSOR BANDWIDTH BY INSERTING INTERRUPT SERVICING INSTRUCTIONS TO INTERVENE MAIN PROGRAM IN INSTRUCTION QUEUE MECHANISM

Mail Stop PATENT APPLICATIONS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTION OF FILING RECEIPT

The Filing Receipt (Exhibit A) for the above-identified application shows the Title as **Method and apparatus for allocating processor bandwidth in heavy-usage environment.**

The Preliminary Amendment, Pages 1-2 of 11, (Exhibit B) filed with this continuation application shows the Title as **ALLOCATION OF PROCESSOR BANDWIDTH BY INSERTING INTERRUPT SERVICING INSTRUCTIONS TO INTERVENE MAIN PROGRAM IN INSTRUCTION QUEUE MECHANISM.**

Applicant requests a correction of the Filing Receipt to reflect the proper Title.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: January 15, 2004

Beth A. Nichols
Beth A. Nichols

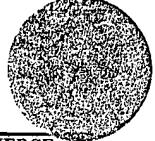
Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Graciela G. Cowger
Reg. No. 42,444

Customer No. 20575

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205
(503) 222-3613



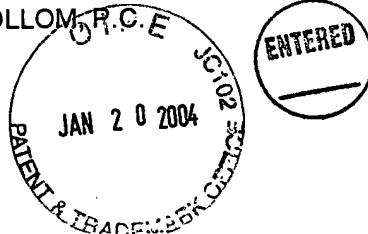
UNITED STATES PATENT AND TRADEMARK OFFICE

66
UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY/DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/698,144	10/31/2003	2181	1136	5038-331	5	26	6

CONFIRMATION NO. 2967

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205



ENTERED

FILING RECEIPT



OC000000011628468

Date Mailed: 01/06/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Douglas D. Boom, Portland, OR;
Matthew M. Gilbert, Hillsboro, OR;

Assignment For Published Patent Application

Intel Corporation (a Delaware Corporation), Santa Clara, CA;

Domestic Priority data as claimed by applicant

This application is a CON of 09/474,834 12/30/1999 PAT 6,662,297

Foreign Applications

If Required, Foreign Filing License Granted: 01/05/2004

Projected Publication Date: 04/15/2004

Non-Publication Request: No

Early Publication Request: No

Title

Method and apparatus for allocating processor bandwidth in heavy-usage environment